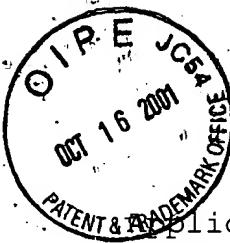


AJ-12823



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al. Art Unit : 2823
 Serial No. : 08/994,038 Examiner : W. David Coleman
 Filed : December 18, 1997
 Title : CHARGE TRANSFER SEMICONDUCTOR DEVICE AND MANUFACTURING
 METHOD THEREOF

Commissioner for Patents
 Washington, D.C. 20231

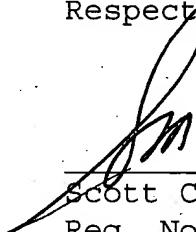
TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
 The required fees are computed below. Please apply any charges
 not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	23	-	23	=	0	\$0
Independent	5	-	5	=	0	<u>\$0</u>
TOTAL FEE DUE						\$0

Respectfully submitted,

Date: 10/1/01


 Scott C. Harris
 Reg. No. 32,030

SCH/smr
 Fish & Richardson P.C.
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 4350 La Jolla Village Drive, Suite 500
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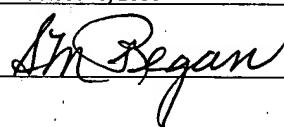
CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 1, 2001

Date of Deposit

Signature


 Susan Regan

Typed or Printed Name of Person Signing Certificate